

Art Unit: 2800

Clmpto

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Claims- 1-15 has been cancel

16. A liquid crystal display, comprising:

a first substrate;

a gate line assembly and a common electrode line assembly formed on the first substrate, the gate line assembly comprising a plurality of gate lines proceeding in the horizontal direction and gate pads connected to the gate lines, the common electrode line assembly comprising common signal lines proceeding parallel to the gate lines and common electrodes connected to the common signal lines while proceeding in the vertical direction;

a gate insulating layer covering the gate line assembly and the common electrode line assembly;

a semiconductor pattern formed on the gate insulating layer;

an ohmic contact pattern formed on the semiconductor pattern;

a data line assembly and pixel electrodes, the data line assembly comprising a plurality of data lines formed on the gate insulating layer and the ohmic contact pattern while crossing over the gate lines to form pixel regions, data pads connected to the data lines, source electrodes being parts of or branched from the data lines and drain electrodes separated from the source electrodes, the pixel electrodes connected to the drain electrodes at the pixel regions while proceeding parallel to the common electrodes;

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a passivation layer covering the data line assembly and the pixel electrodes;  
and

a second substrate facing the first substrate;

wherein the gate insulating layer and the passivation layer exposed externally to the second substrate and the data pads are removed.

17. The liquid crystal display of claim 16, wherein the ohmic contact pattern has the same shape as the data line assembly and the pixel electrodes, and the semiconductor pattern has the same shape as the data line assembly and the pixel electrodes except for the area between the source and the drain electrodes.

18. The liquid crystal display of claim 16, further comprising subsidiary data lines placed at the same plane as the gate line assembly with the same material, and first contact holes formed on the gate insulating layer while exposing the subsidiary data lines.

19. The liquid crystal display of claim 18, wherein each pixel electrode is formed with two or more linear electrode portions, and each linear electrode portion is positioned close to the neighboring data line.

20. The liquid crystal display of claim 19, wherein both ends of the linear electrode portions are connected to each other such that the pixel electrode bears a ring shape.

21. The liquid crystal display of claim 20, further comprising one or more light interception patterns separated from the gate lines and positioned close to the subsidiary data lines while proceeding parallel to the subsidiary data lines.

22. The liquid crystal display of claim 21, wherein the pixel electrodes are

partially overlapped with the light interception patterns.

23. The liquid crystal display of claim 21, wherein the pixel electrodes are

claims-24-36 has been cancel